

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Cohen et al.

Serial No.: 10/629,014

Group Art Unit: 2811

Filed: July 29, 2003

Examiner: Gebremariam, Samuel A.

For: DOUBLE-GATE FET WITH PLANARIZED SURFACES AND SELF ALIGNED SILICIDES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF CORRECTED DRAWINGS

Sir:

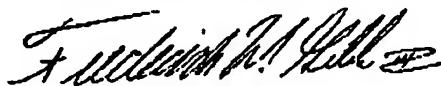
In response to the Office Action dated December 4, 2003, submitted herewith are two (2) sheets of formal drawings comprising Figs. 9 and 11 for the above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please substitute these formal drawings for the same numbered drawings which were filed with the application.

The only change from the previously filed drawings is the inclusion of spacers 307 in Figures 9 and 11. Applicants note that an inadvertent figure copying error occurred between Figure 7 and Figure 9 wherein the spacers 307 were inadvertently excluded from Figure 9. This caused the spacers 307 to also not appear in Figure 11. Applicants submit that the corrections to Figures 9 and 11 are obvious drawing errors that would be recognized by one ordinarily skilled in the art, and do not present new matter.

More specifically, the material surrounding all sides of the spacers 307 does not change between Figure 7 and 9. As can be seen when comparing Figures 7 and 9, the oxide regions 311,

309 and the polysilicon region 502 are not altered between Figures 7 and 9. Therefore, the spacers 307 could not have been altered and clearly should have been included by the draftsman in Figure 9. In addition, the discussion appearing on page 9, line 8-page 10, line 2 is silent regarding any removal process that would affect the spacers 307. Therefore, it would be clear to one ordinarily skilled in the art that spacers 307 should have been included by the draftsman in Figures 9 and 11 and that the corrected drawings being submitted herewith do not add new matter.

Respectfully Submitted,



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Date: 3/3/04
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